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ACK	<i>Acknowledge</i>
ARAT	<i>Active Reader Active Tag</i>
ARPT	<i>Active Reader Passive Tag</i>
CPU	<i>Central Processing Unit</i>
FF	<i>Flip Flop</i>
FPGA	<i>Field Programmable Gate Array</i>
HDL	<i>Hardware Description Language</i>
IC	<i>Integrated Circuit</i>
IOB	<i>Input/Output Blocks</i>
LLC	<i>Level Logic Converter</i>
LSB	<i>Least Significant Bit</i>
MSB	<i>Most Significant Bit</i>
NFC	<i>Near Field Communication</i>
PISO	<i>Paralel In Serial Out</i>
PIPO	<i>Paralel In Paralel Out</i>
PRAT	<i>Passive Reader Active Tag</i>
RAM	<i>Random Access Memory</i>
RF	<i>Radio Frequency</i>
RFID	<i>Radio Frequency Identify</i>
SISO	<i>Serial In Serial Out</i>
SIPO	<i>Serial In Paralel Out</i>
SSO	<i>Seven-Segment Out</i>
STB	<i>Strobe</i>
UART	<i>Universal Asynchronous Receiver Transmitter</i>
UID	<i>Unique ID</i>
VHDL	<i>VHSIC Hardware Description Language</i>
VHSIC	<i>Very High Speed Integrated Circuit</i>
XOR	<i>Exclusive OR</i>