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BPSK	<i>Binary Phase Shift Keying</i>
CCDF	<i>Complementary Cumulative Distribution Function</i>
CLB	<i>Configure Logic Blocks</i>
DWT	<i>Discrete Wavelet Transform</i>
FFT	<i>Fast Fourier Transform</i>
FPGA	<i>Field-Programmable Gate Array</i>
GI	<i>Guard Interval</i>
HPF	<i>High Pass Filter</i>
IC	<i>Integrated Circuit</i>
IDWT	<i>Inverse Discrete Wavelet Transform</i>
IEEE	<i>Institute of Electrical and Electronics Engineers</i>
IFFT	<i>Inverse Fast Fourier Transform</i>
ISI	<i>Inter Symbol Interference</i>
LPF	<i>Low Pass Filter</i>
LUT	<i>Look-Up Table</i>
OFDM	<i>Orthogonal Frequency Division Multiplexing</i>
OWDM	<i>Orthogonal Wavelet Division Multiplexing</i>
PAPR	<i>Peak to Average Power Ratio</i>
P/S	<i>Parallel to Serial</i>
RAM	<i>Random Access Memory</i>
S/P	<i>Serial to Parallel</i>
VHDL	<i>VHSIC Hardware Description Language</i>
VHSIC	<i>Very High Speed Integrated Circuit</i>