

DAFTAR PUSTAKA

- [1] Nvidia White Paper,“*NVIDIA’s Next Generation CUDATM Compute Architecture:FERMI.*”. 2009.
- [2] John D. Owens, Mike Houston, David Luebke, Simon Green, John E. Stone, and James C. Phillips,”*GPU Computing : Graphics Processing Units-powerful, programmable, and highly parallel-are increasingly targeting general-purpose computing applications.*” Vol. 96, No. 5, May 2008.
- [3] Jayshree Ghorpade¹, Jitendra Parande, Madhura Kulkarni, Amit Bawaskar. “*GPGPU Processing in CUDA Architecture.*” Vol.3, No.1, January 2012.
- [4] Nvidia.“*CUDA C Programming Guide.*“September 2016
- [5] Intel White Paper.”*Introduction to Intel Architecture.*” 2014.
- [6] S. S. Navalgund, Akshay Desai, Krishna Ankalgi, and HarishYamanur .“*Parallelization of AES Algorithm Using OpenMP.*” Lecture Notes on Information Theory Vol. 1, No. 4, December 2013.
- [7] Axiomtek White Paper.” *Axiomtek Haswell/ Haswell Refresh Products.*” 2015.
- [8] Svetlin A. Manavski.”*CUDA Compatible GPU as an Effecient Hardware Accelerator for AES Cryptography.*” Dubai, United Arab Emirates. 24-27 November 2007.
- [9] Qinjian Li, Chengwen Zhong.”*Implementation and Analysis of AES Encryption on GPU.*” Hong Kong Baptist University, Hong Kong, CHINA.
- [10] Michael Kipper, Joshua Slavkin, Dmitry Denisenko. ”*Implementing AES on GPU Final Report.*” April 20, 2009. University of Toronto.
- [11] Angelo Barnes, Ryan Fernando, Kasuni Mettananda and Roshan Ragel.”*Improving the Throughput of the AES Algorithm with Multicore Processors.*” University of Peradeniya, Peradeniya 20400, Sri Lanka.
- [12] Supachai Thongsuk , Prabhas Chongstitvatana, Ph.D.” *An implementation of AES algorithm on multicore processors for high throughput.*” Chulalongkorn University Bangkok, Thailand. 2014.