

DAFTAR SINGKATAN

CLB	:	Configurable Logic Block
DWT	:	Discrete Wavelet Transform
DUT	:	Design Under Test
FPGA	:	Field Programmable Gate Array
HDL	:	Hardware Description Language
IOB	:	Input/Output Block
JFIF	:	JPEG File Interchange Format
JPEG	:	Joint Photographic Experts Group
LUT	:	Look Up Table
PLD	:	Programmable Logic Device
RAM	:	Random Access Memory
ROM	:	Read Only Memory
SOC	:	System On a Chip
VHDL	:	Very High Speed IC Hardware Description Language
VLSI	:	Very Large Scale Integrated Circuit