

## DAFTAR PUSTAKA

- [1] Muh Syafik Irsyadi. 2010. Desain dan Implementasi 2k Pipeline FFT/IFFT Core untuk DVB-T [Institut Teknologi Bandung] Skripsi.
- [2] H, Renato Neuenfeld; Mateus B Fonseca; Eduardo A.C.da Costa; Jean P Oses. 2017. *Exploiting Addition Schemes for the Improvement of Optimized Radix-2 and Radix-4 FFT Butterflies*. Paper. IEEE
- [3] Charles Wu. 1998. Implementing the Radix-4 Decimation in Frequency (DIF) Fast Fourier Transform (FFT) Algorithm Using a TMS320C80 DSP: Texas Instruments Incorporated
- [4] F, Matteo.; Steven G. Johnson. 1997. *The Fast Fourier Transform in The West*. Massachusetts Institut of Technology
- [5] Riyanto. Sugeng; Agus Purwanto; Supardi. 2009. Lagoritma Fast Fourier Transform (FFT) Decimation In Time (DIT) Dengan Resolusi 1/10 Hertz. Prosiding Seminar Nasional. Yogyakarta: Universitas Negeri Yogyakarta
- [6] Dianputra. Robby; Diah Puspitaningrum; Ernawati. 2014. Implementasi Algoritma Fast Fourier Transform Untuk Pengolahan Sinyal Digital Pada Tuning Guitar Dengan Open String. Jurnal. Bengkulu: Universitas Bengkulu
- [7] Yonemoto. A; T. Hisakado; K. Okumura. 2003. Accuracy Improvement of The FFT-Based Numerical Inversion of Laplace Transform. Paper. IEE
- [8] Anonim. "FPGA". <https://id.wikipedia.org/wiki/FPGA>. (diakses tanggal 6 Oktober 2016)
- [9] MathWorks. "MATLAB". <https://www.mathworks.com/products/matlab/>. (diakses tanggal 6 Oktober 2016)
- [10] Cooley. James W. 1987. *The Re-Discovery of The Last Fourie Transform Algoritma*. MikroChimica Acta
- [11] Wang. Sying-Jyang; Niraj K.Jha. 1994. *Algorithm-Based Fault Tolerance For FFT Network*. Paper. IEE
- [12] Manalu. Manato J F. 2011. *Perancangan Dan Implementasi Prosesor I/FFT 512 Titik Radix-8 Pada FPGA*[Institut Teknologi Telkom]Skripsi.
- [13] Ferry Wahyu Wibowo. "VHDL". <https://id.wikipedia.org/wiki/VHDL>. (diakses tanggal 10 Oktober 2016)

- [14] Ardhy. “Penjelasan Mengenai Pemrograman VHDL”.  
<http://marikitareview.blogspot.co.id/2014/01/pemrograman-vhdl.html> .  
(diakses 20 Oktober 2016)
- [15] J.J. Zhang; Z.H. Tang; R.P. Giddings; Q. Wu; W.L. Wang; B.Y. Cao; Q.W. Zhang; J.M. Tang. 2016. *Stage-dependent DSP Operation Range Clipping-induced Bit Resolution Reductions of Full Parallel 64-point FFTs Incorporated in FPGA-based Optical OFDM Receivers*. Paper. IEE