

## **PREFACE**

The writer wants to thank to Almighty God Jesus Christ because of His bless and grace, she can finish this paper. This paper titled " Machine Instruction Analysis for DCT Algorithm using DLX Architecture ". The writer wrote it to fulfill the final project for her S-1 Degree in Telecommunication Engineering Study Program, Faculty of Electrical Engineering, Telkom University Bandung.

This thesis presents a Discrete Cosine Transform compression of image coding method with DLX microprocessor using MIPS assembly language. The aim of this experiment is to analysis the machine instructions of Discrete Cosine Transform algorithm using DLX Architecture.

The author hopes all the best achieved from this thesis for further development in the future.

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