

DAFTAR SINGKATAN

AM	:	<i>Amplitude Modulation</i>
CLB	:	<i>Configurable Logic Block</i>
DAC	:	<i>Digital to Analog Converter</i>
DDS	:	<i>Direct Digital Synthesiser</i>
ERP	:	<i>Effective Radiated Power</i>
FM	:	<i>Frequency Modulation</i>
FPGA	:	<i>Field Programmer Gate Array</i>
GPIO	:	<i>General Purpose Input Output</i>
HDL	:	<i>Hardware Description Language</i>
I/O	:	<i>Input/Output</i>
ILA	:	<i>Integrated Logic Analyzer</i>
IOB	:	<i>Input Output Blocks</i>
LPF	:	<i>Low Pass Filter</i>
LUT	:	<i>Look Up Table</i>
NCO	:	<i>Numerically Control Oscillator</i>
NGC	:	<i>Native Generic Circuit</i>
NGD	:	<i>Native Generic Database</i>
PAR	:	<i>Place and Route</i>
PCF	:	<i>Physical Constraints File</i>
PLL	:	<i>Phase Locked Loop</i>
ROM	:	<i>Read Only Memory</i>
VCO	:	<i>Voltage Control Oscillator</i>
VHF	:	<i>Very High Frequency</i>
VHDL	:	<i>VHSIC Hardware Description Language</i>
UART	:	<i>Universal Asynchronous Receiver-Transmitter</i>
USB	:	<i>Universal Serial Bus</i>